

FIG 1
PRIOR ART

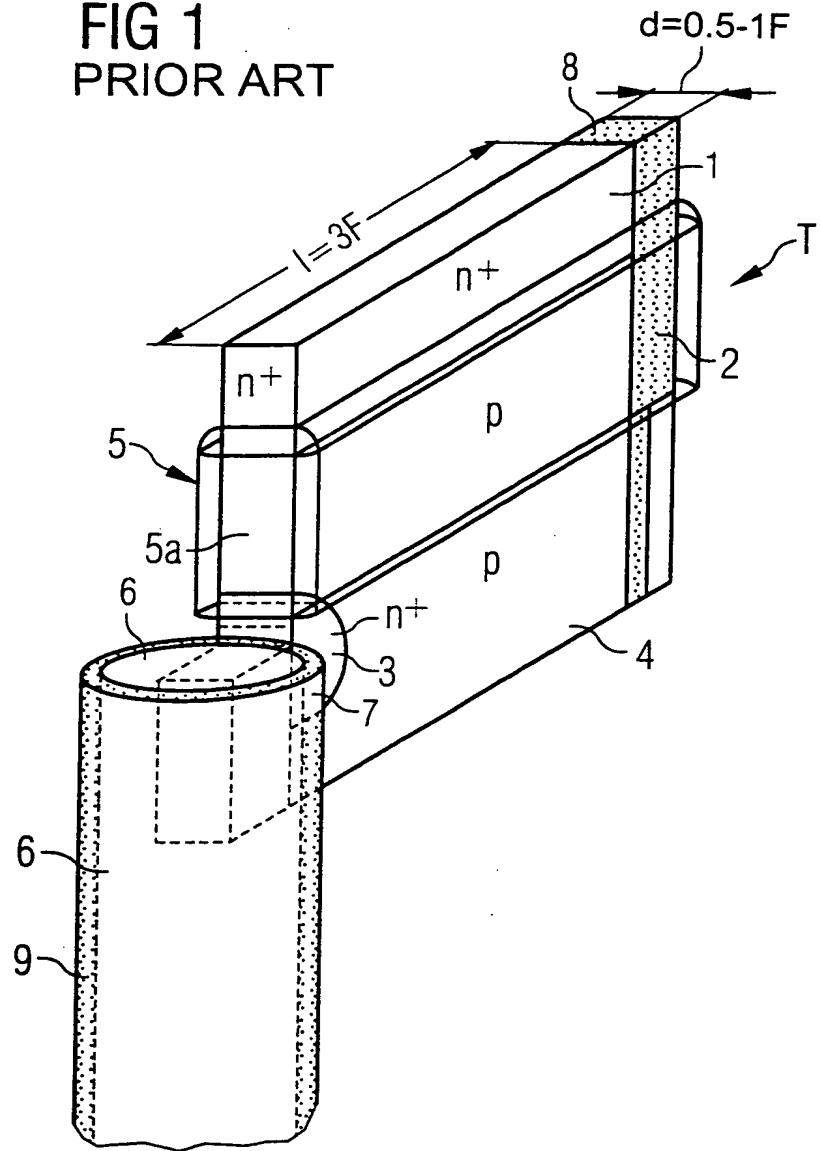


FIG 2
PRIOR ART

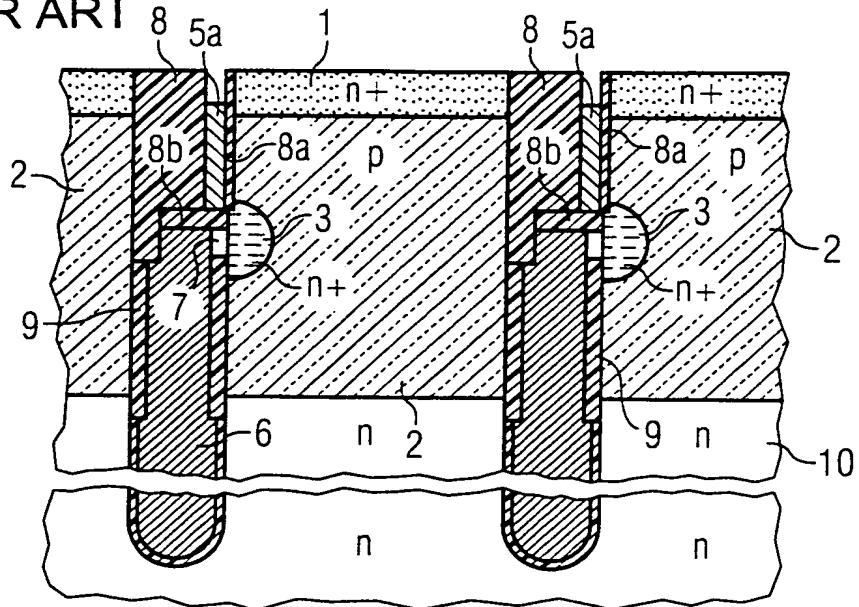
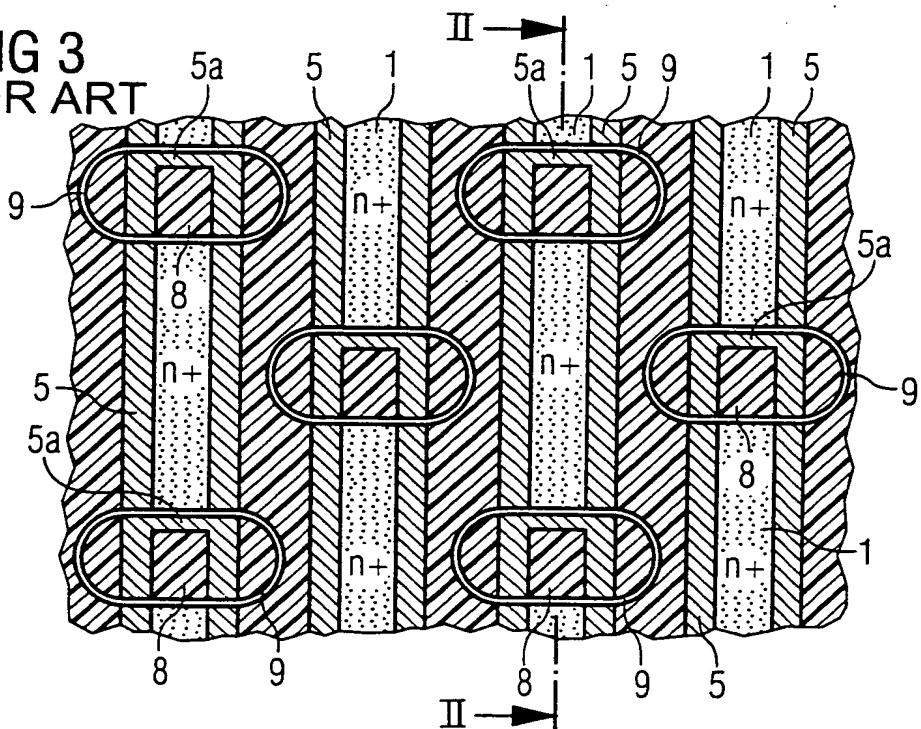


FIG 3
PRIOR ART



Gate

p-Region

Isolation/
Dielectric

Webs (AT)
(Source-Region)

Poly-Si

Drain-Region

3/4

FIG 4A

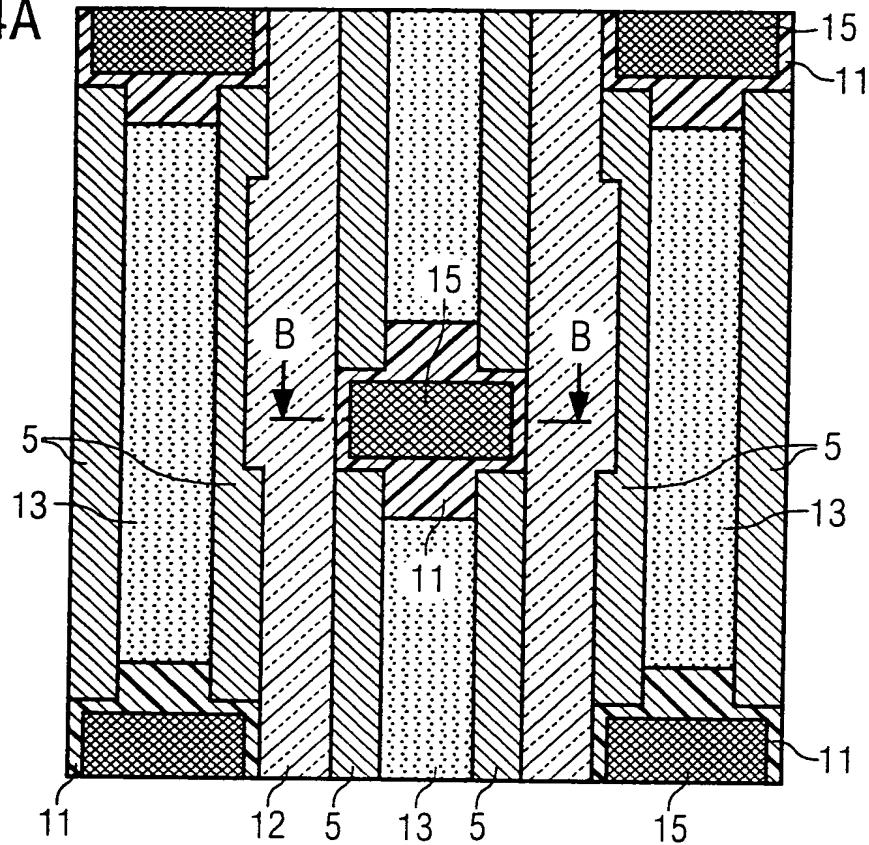


FIG 4B

Section B-B

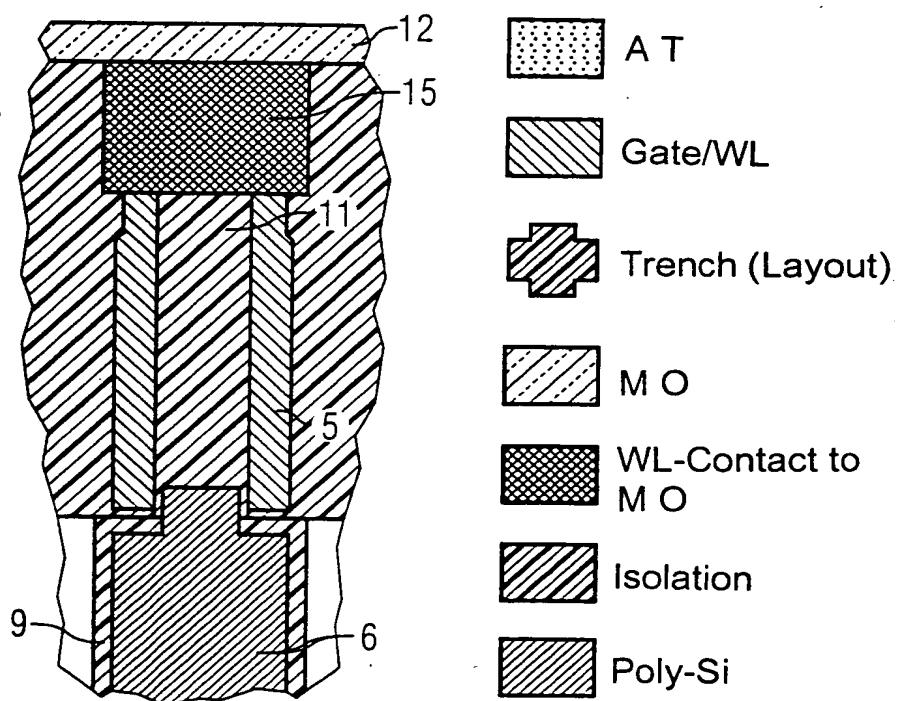


FIG 5

